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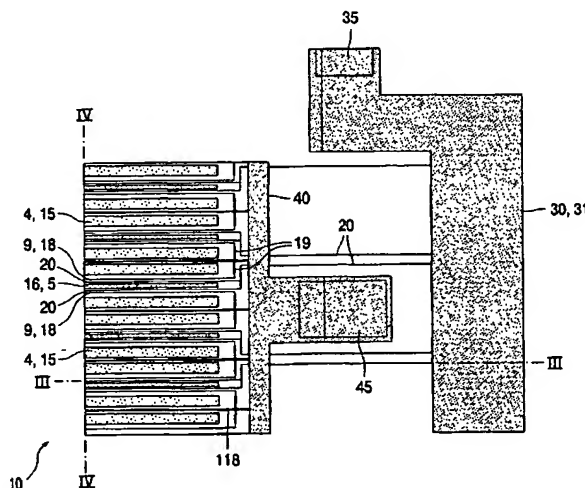
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[Continued on next page]

(54) Title: SEMICONDUCTOR DEVICE COMPRISING A FIELD-EFFECT TRANSISTOR AND METHOD OF OPERATING
THE SAME



(57) Abstract: The invention relates to in particular a lateral DMOST with a drain extension (8). In the known transistor a further metal strip (20) is positioned between the gate electrode contact strip and the drain contact (16) which is electrically connected with the source region contact (15). In the device proposed here, the connection between the further metal strip (20) and the source contact (15,12) comprises a capacitor (30) and the further metal strip (20) is provided with a further contact region (35) for delivering a voltage to the further metal strip (20). In this way an improved linearity is possible and the usefulness of the device is improved in particular at high power and at high frequencies. Preferably the capacitor (30) is integrated with the transistor in a single semiconductor body (1). The invention further comprises a method of operating a device (10) according to the invention.

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**Declaration under Rule 4.17:**

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B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE 28 55 844 A (TEXAS INSTRUMENTS DEUTSCHLAND) 26 June 1980 (1980-06-26) the whole document	1-11
A	US 2002/033508 A1 (NAGURA KENICHI ET AL) 21 March 2002 (2002-03-21) the whole document	1-11
A	EP 0 309 748 A (SIEMENS AG) 5 April 1989 (1989-04-05) the whole document	1-11

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☒ Patent family members are listed in annex.

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